

Abstract

The present invention is a data transfer circuit applicable to a liquid crystal display apparatus with a drive circuit formed integral, for example, on an insulation substrate, configured such that only an inverted output of a latch result of a first latch section (41) or only a non-inverted output thereof is data-transferred to a second latch section (42), and that at least during a period of data transfer to the second latch section (42), a power supply voltage of the first latch section (41) is raised.

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